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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Ohtani, et al

Art Unit: 1765

Serial No.: 08/690,747

Examiner : Robert Kunemund Confirmation No.: 1369

Filed Title

: August 1, 1996 : METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE

Commissioner for Patents Washington, D.C. 20231

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed. Please apply the required fees computed below, any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total claims

62 62

0

\$0

Independent

13

13

0

\$0

Applicant hereby petitions under 37 C.F.R. \$1.136 for a 1 month extension of time.

\$110

TOTAL FEE DUE

\$110

Respectfully submitted,

32,030 . No.

PTO Customer No. 20985

Fish & Richardson P.C.

4350 La Jolla Village Drive, Suite 500 San Diego, California 92122

Telephone: (858) 678-5070

Facsimile:

(858) 678-5099

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